

Notice of References Cited	Application/Control No. 10/579,383		Applicant(s)/Patent Under Reexamination KULKARNI ET AL.	
	Examiner Paul R. Myers		Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,447,878 A	05-1984	Kinnie et al.	710/307
*	B	US-4,667,305 A	05-1987	Dill et al.	710/307
*	C	US-4,878,166 A	10-1989	Johnson et al.	710/307
*	D	US-5,388,227 A	02-1995	McFarland, Harold L.	710/307
*	E	US-5,561,772 A	10-1996	Takano et al.	710/300
*	F	US-5,790,874 A	08-1998	Takano et al.	713/320
*	G	US-5,901,294 A	05-1999	Tran et al.	710/107
*	H	US-6,618,777 B1	09-2003	Greenfield, Zvi	710/120
*	I	US-7,085,875 B1	08-2006	Yona et al.	710/307
*	J	US-7,257,661 B2	08-2007	O'Donnell, Ciaran Gerard	710/305
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	GB 2375695 A	11-2002	United Kingdom	MYCROFT et al.	G06F 01/32
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Reordering Memory Bus Transactions for Reduced PowerConsumption" by Childers et al pages 1-9 June 2000
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.